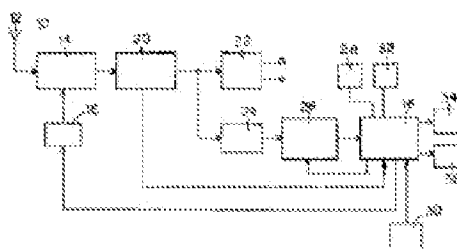


FM MULTIPLEX RECEIVER**Publication number:** JP8162985**Publication date:** 1996-06-21**Inventor:** HATTORI YUZO**Applicant:** SANYO ELECTRIC CO**Classification:****- international:** **H04J1/05; H04B1/16; H04B1/16; H04J1/00; H04B1/16;**
H04B1/16; (IPC1-7): H04B1/16; H04J1/05**- European:****Application number:** JP19940297160 19941130**Priority number(s):** JP19940297160 19941130*Report a data error here***Abstract of JP8162985**

PURPOSE: To perform a display without requiring a troublesome operation by providing an information storage means, a control means reading the information which is pertinent to a preliminarily set condition from the information storage means and a means displaying the information read out from the storage means. **CONSTITUTION:** The digital signal demodulated in an LMSK(Level-controlled Minimum Shift Keying) demodulation circuit 24 is synchronized and reproduced in synchronizing reproduction circuit and error correction circuit 26 in accordance with a BIC(block identification code), an error correction is performed for the digital signal in accordance with a parity code and the signal is imparted to a microcomputer 16. After the digital signal imparted from the microcomputer 16 is temporarily written in a RAM 28, the digital signal is processed by the key input signal from an operation key 30 and the program stored in a ROM 32 and character information is displayed on a display device 34. At the same time, preliminarily designated information or information on weather information, for instance, is always fetched by the microcomputer 16 and is stored in the RAM 28. This weather information is updated only when new different information is received. Thus, the weather information is always displayed by a display means without requiring a troublesome operation.



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